



# Processor Block Diagram

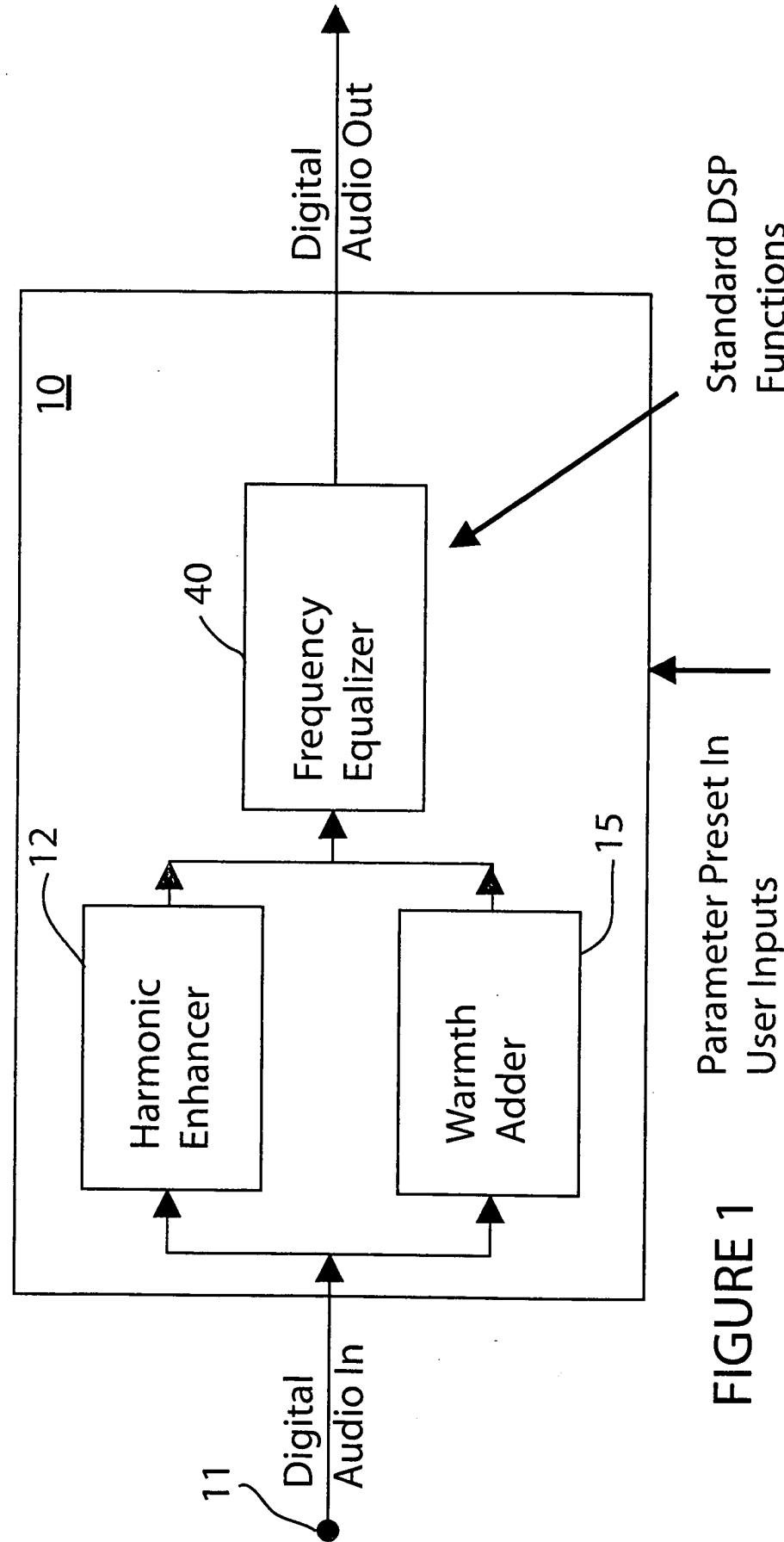


FIGURE 1

Parameter Preset In  
User Inputs

Standard DSP  
Functions

# Harmonic Enhancer Diagram

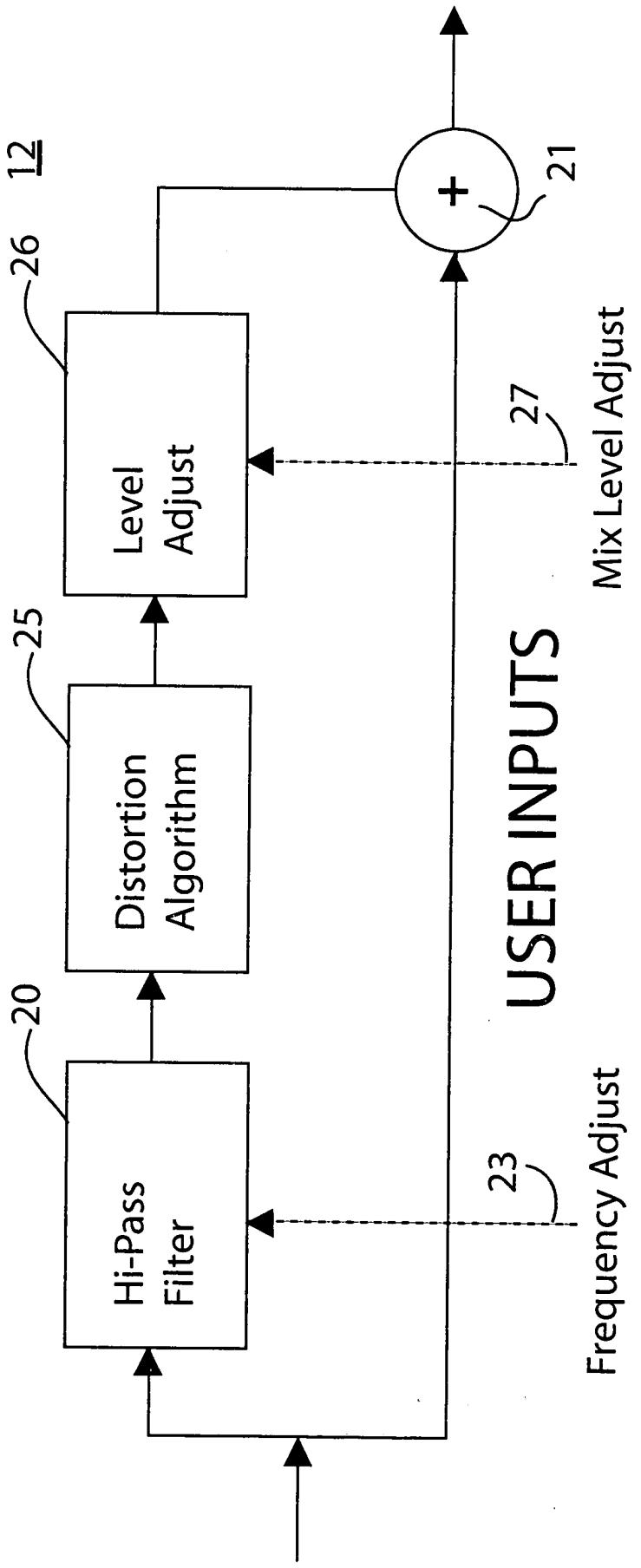
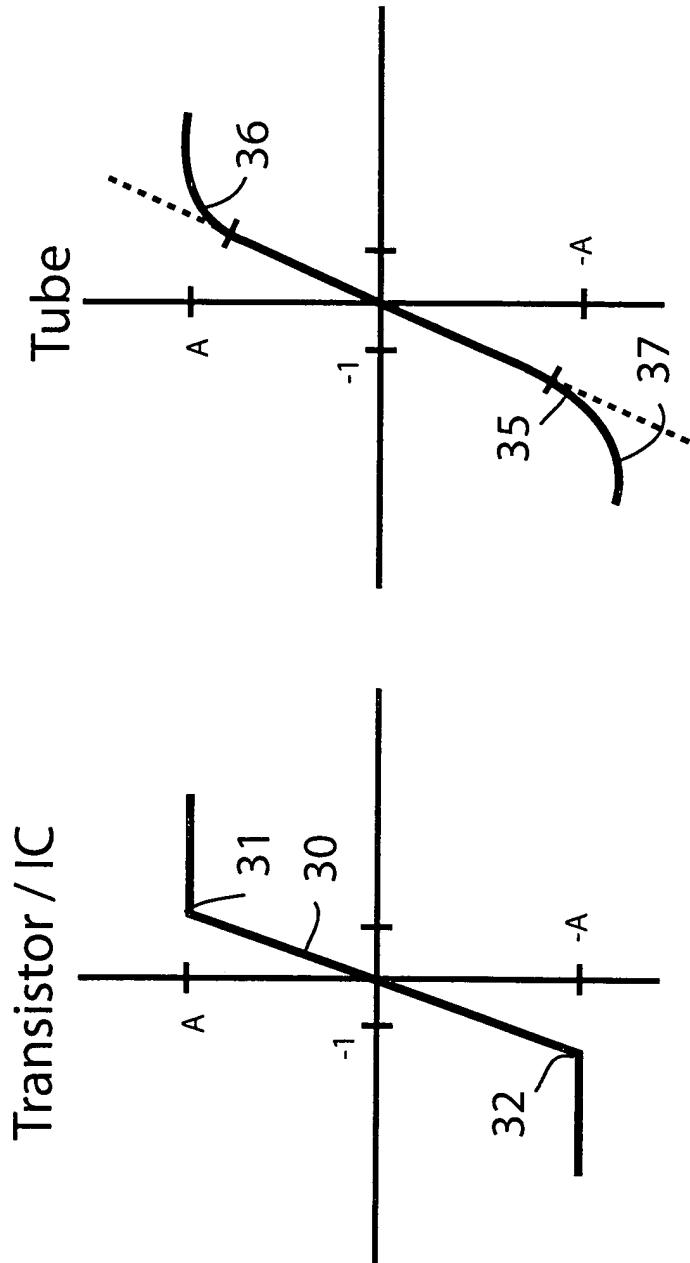
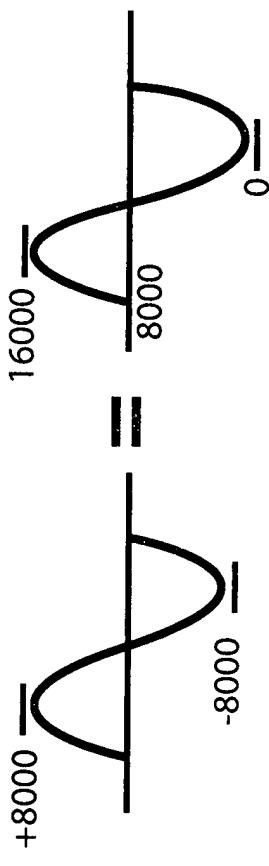


FIGURE 2

**FIGURE 3** Warmth Adder



**FIGURE 4**



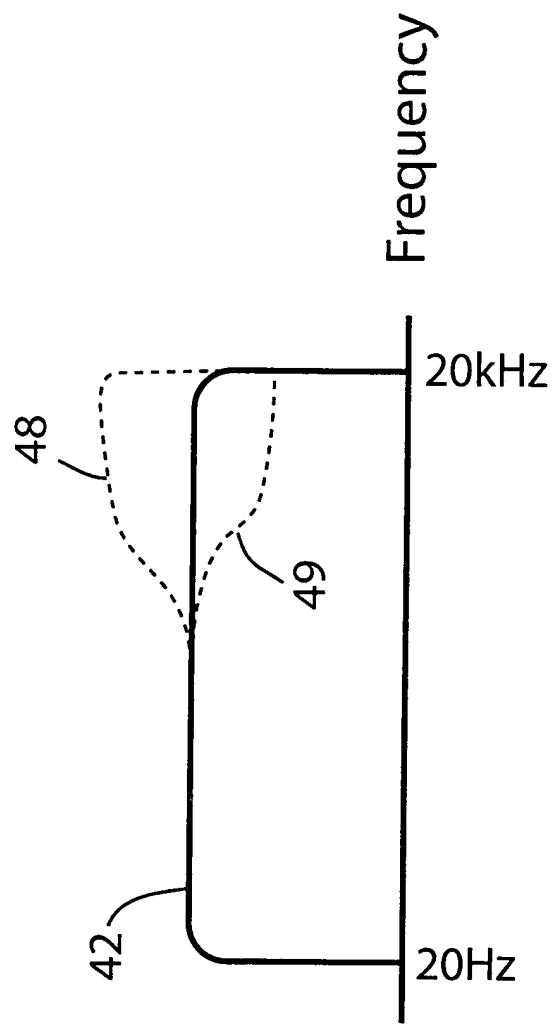


FIGURE 5

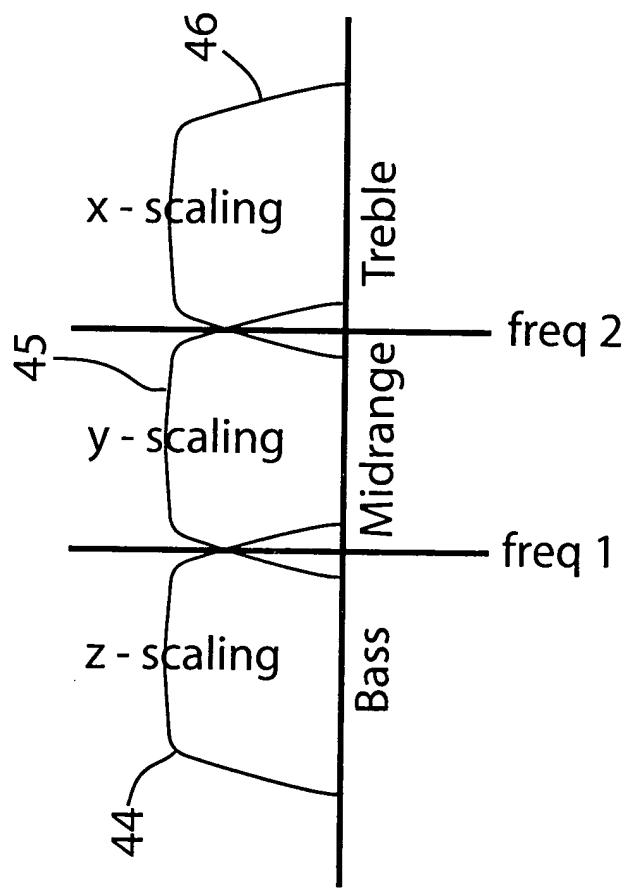


FIGURE 6

# Processor Block Diagram

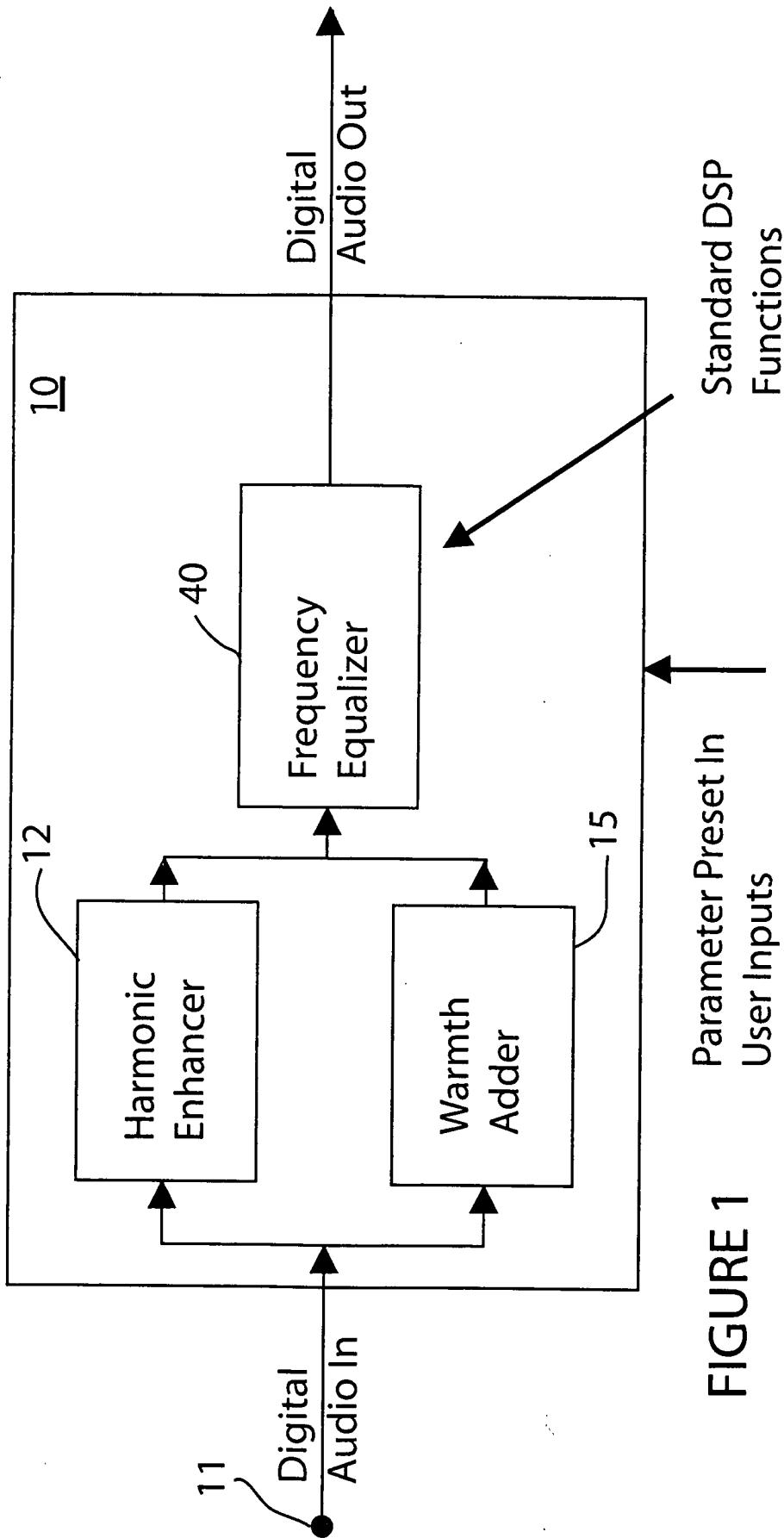


FIGURE 1

Parameter Preset In  
User Inputs

Standard DSP  
Functions

# Harmonic Enhancer Diagram

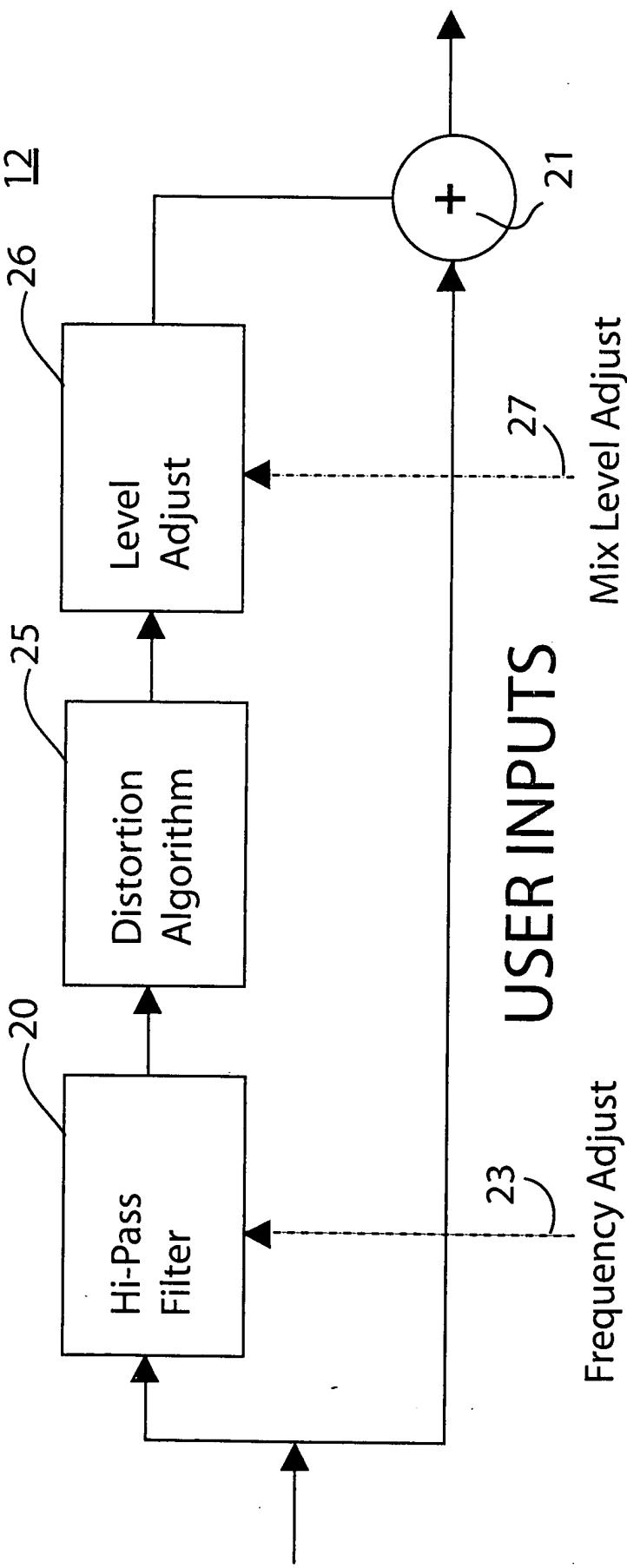
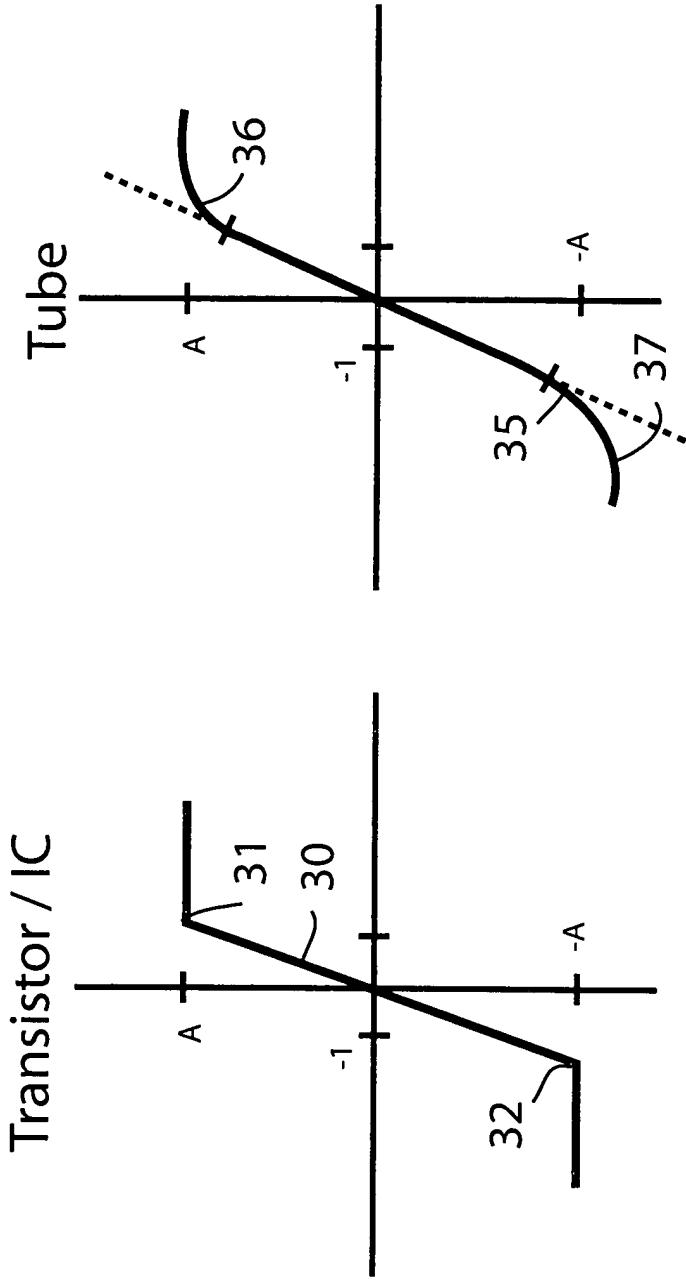
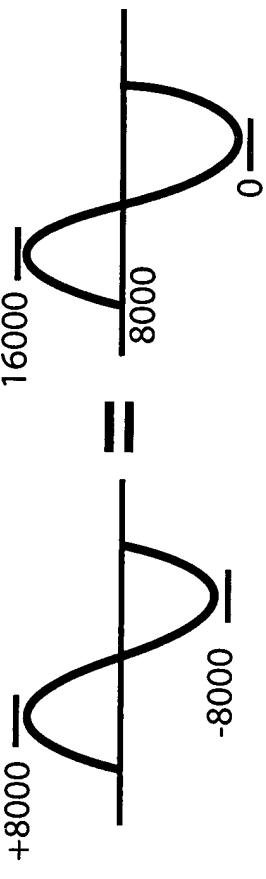


FIGURE 2

**FIGURE 3** Warmth Adder



**FIGURE 4**



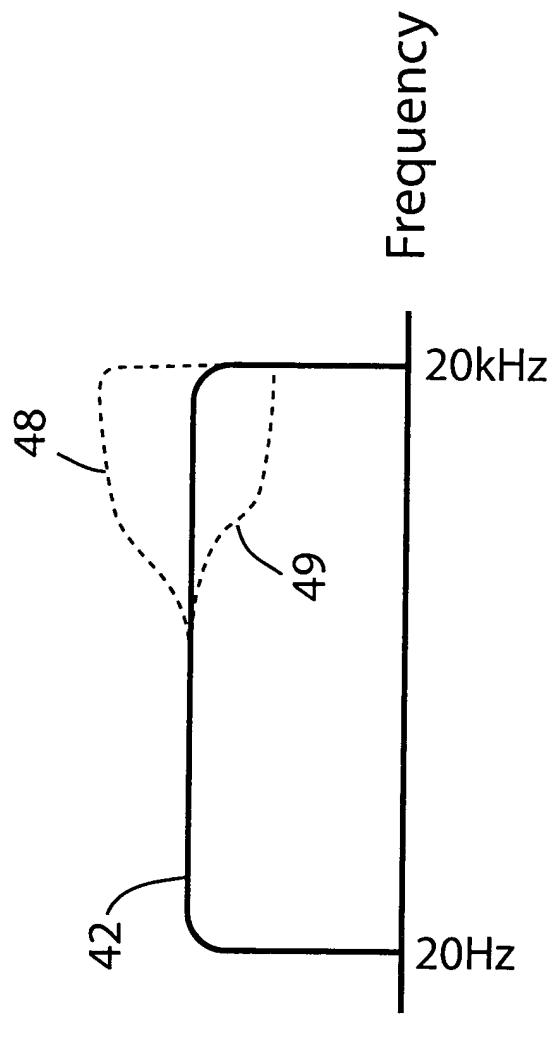


FIGURE 5

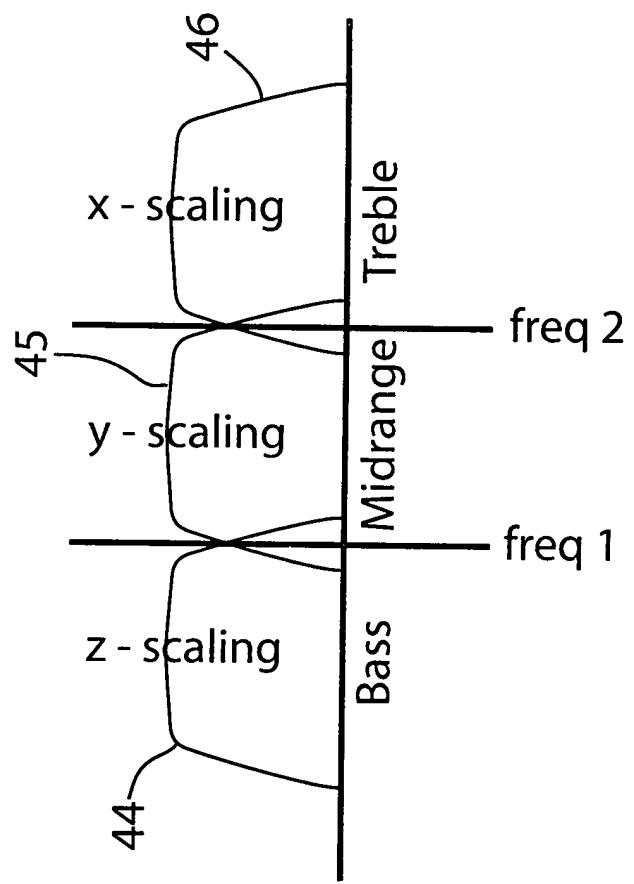


FIGURE 6